

CLAIMS

1. A driving apparatus for a plasma display panel, comprising:

5 a set-up supplier for supplying a rising ramp waveform to scan electrodes in an initialization period and for supplying a positive enhancing pulse to the scan electrodes during an enhancing period following said initialization period; and

10 a negative voltage supplier for supplying a falling ramp waveform to the scan electrodes in the initialization period and for supplying a negative enhancing pulse to the scan electrodes during the enhancing period.

15 2. The driving apparatus as claimed in claim 1, wherein the negative voltage supplier includes only a single of switching device.

3. The driving apparatus as claimed in claim 1, wherein
20 the negative voltage supplier includes:

a switching device provided between one terminal of a drive integrated circuit and a scan voltage source; and

a variable resistor connected to a gate terminal of the switching device to limit a channel width of the
25 switching device.

4. The driving apparatus as claimed in claim 1, wherein said negative enhancing pulse falls until a voltage higher than a voltage value of said falling ramp waveform.

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5. The driving apparatus as claimed in claim 3, wherein the switching device keeps a turn-on state from a period at which said negative enhancing pulse is supplied until an address period.

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6. A method of driving a plasma display panel in which one frame has a plurality of sub-fields, wherein any at least one of sub-fields included in said frame is comprised of:

an initialization period for forming wall charges at
15 all of discharge cells;

a first enhancing period for supplying a positive enhancing pulse to a scan electrode such that desired wall charges can be formed at all the discharge cells;

a second enhancing period for supplying a negative
20 enhancing pulse after said positive enhancing pulse was supplied;

an address period for causing an address discharge so as to select said discharge cell; and

a sustain period for causing a predetermined frequency
25 of sustain discharge according to a gray level value at the

discharge cells at which said address discharge occurs.

7. The method as claimed in claim 6, wherein said initialization period is divided into a set-up interval and
5 a set-down interval, and a rising ramp waveform rising at a slope from a sustain voltage until a sum voltage of said sustain voltage with a set-up voltage is supplied in the set-up period while a falling ramp waveform falling at a slope from said sustain voltage until a negative voltage in
10 the set-down period.

8. The method as claimed in claim 7, wherein said negative enhancing pulse falls until a voltage higher than said negative voltage at a slope.